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UNIVERSITI TUN HUSSEIN ONN MALAYSIA

FINAL EXAMINATION SEMESTER I SESSION 2012/2013

COURSE NAME : CONTROL SYSTEM COURSE CODE : DAE 32103

PROGRAMME : 3 DAE / DAL

EXAMINATION DATE : OCTOBER 2012

DURATION

: 2 ½ HOURS

INSTRUCTION : ANSWER FOUR (4) QUESTIONS ONLY.

THIS PAPER CONSISTS OF EIGHT (8) PAGES

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Q1 (a) Describe the meaning of (i) Control system. (ii) Control system component. (5 marks) (b) Give the advantages and disadvantages of the following control system (i) Open loop control system. Closed loop control system. (ii) (14 marks) (c) There are six (6) steps involved in designing a control system. Briefly describe the control system design process. (6 marks)

Q2 (a) List three (3) types of time domain input function and sketch the graph respectively.

(6 marks)

(b) Solve the ramp response for a system whose transfer function is

$$G(s) = \frac{s}{(s+4)(s+8)}$$

(15 marks)

(c) Draw the free-body diagram of the spring mass damper system as shown in Figure Q2(c).

(4 marks)

Q3 (a) The polynomial function is given.

$$F(s) = \frac{(s+2)(s-3)(s-6)}{(s+3)(s-1)}$$

Plot the poles and zeros for the system.

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(5 marks)

(b) The transfer function of a position control system is given by;

$$\frac{\theta_0(s)}{\theta_i(s)} = \frac{100}{s^2 + 18s + 100}$$

Calculate the damping ratio, (ζ) and natural frequency, (ω_n). State the types of system response then solve;

(i) Peak time t_p

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- (ii) Percent overshoot, $\% M_p$
- (iii) Rise time, t_r
- (iv) Settling time t_s for 5% criterion

(20 marks)

Q4 (a) Data acquisition is the process of sampling signals that measure real world physical conditions and converting the resulting samples into digital numeric values that can be manipulated by a computer.

- i) Sketch a complete block diagram of data acquisition system.
- ii) Explain the function of each component based on your block diagram.

(16 marks)

(b) Calculate the output of 10-bit ADC when the input voltage V_{in} is 2.5 V and reference voltage, V_{ref} is 5 V.

(4 marks)

(c) Sketch a schematic diagram of n-bit DAC using R-2R ladder circuit.

(5 marks)

Q5 (a) List four (4) types of ADC that most frequently used.

(4 marks)

(b) The main function of a digital controller is to control the closed loop response of a system. Sketch a structure of a digital controller.

(5 marks)

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(c) There are three (3) possibilities spectrum of a sampled signal according to Shannon's sampling theorem. Identify and sketch the three (3) possibilities spectrum.

(6 marks)

(d) Consider Figure Q5(d) is an example of Digital Control System which utilizing a microprocessor-based controller with parallel ports. This system has one output port and three input ports (each port has each own address). Based on Figure Q5(d), explain the system operation of this system.

(10 marks)

Q6 (a) Give three (3) types of process control loop other than open loop and closed loop.

(3 marks)

(b)	(i)	List three (3) types of temperature measurement.	
	(ii)	Give one example respectively.	

(6 marks)

(c) (i) Give two (2) types of thermistor.

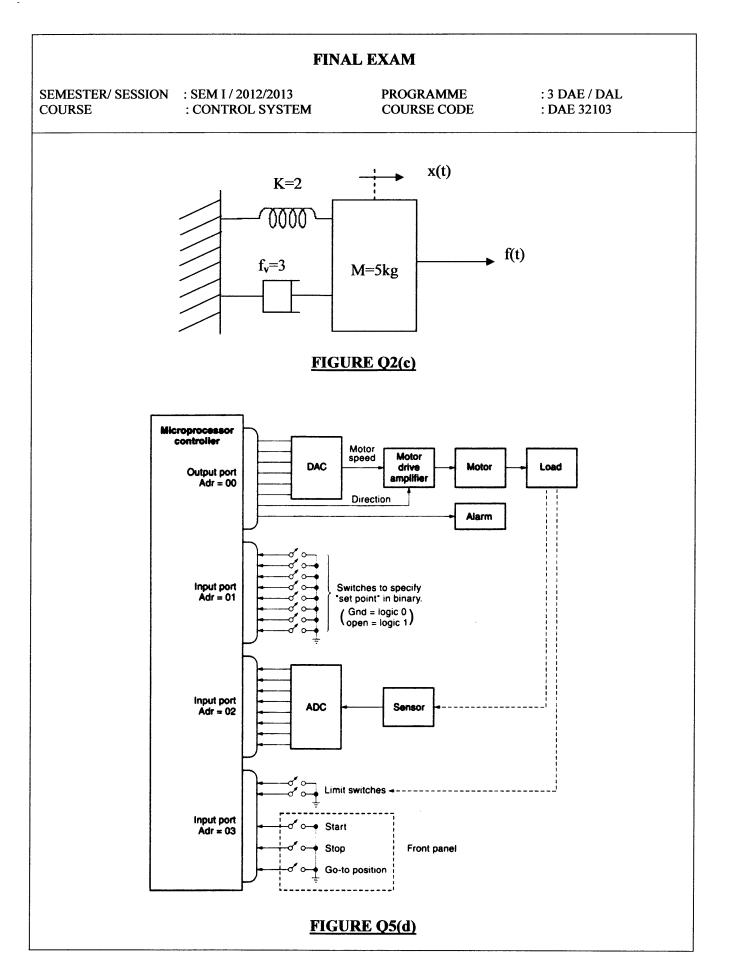
(iii) Explain the relation between the temperature and the resistance value for each type of thermistor.

(4 marks)

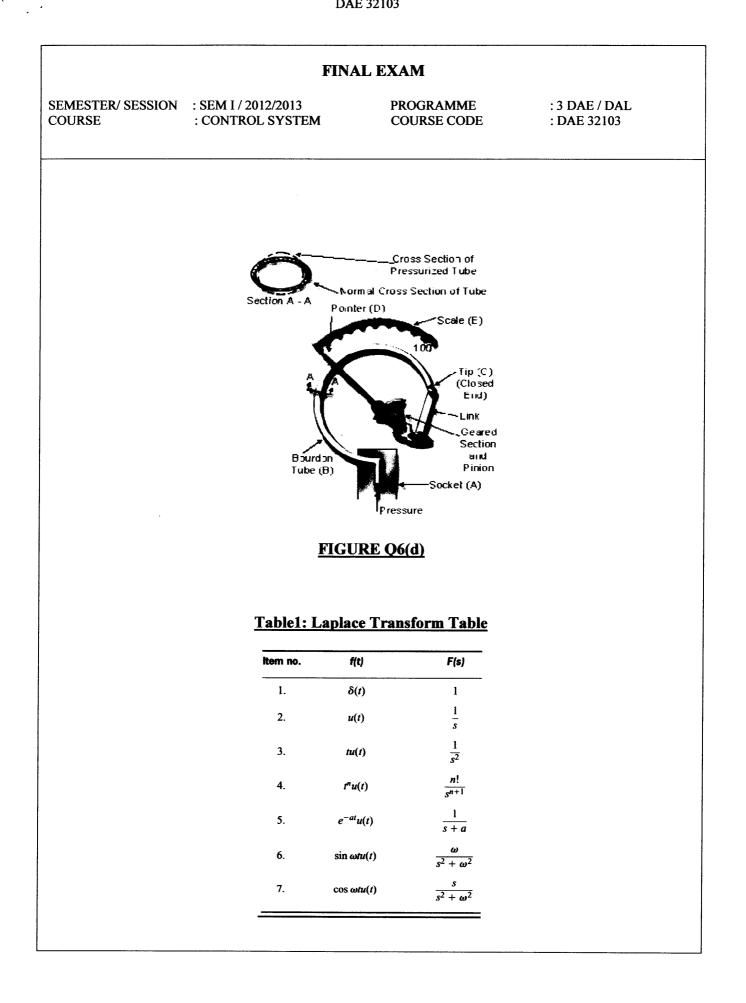
(d) Explain Bourdon Tube working operation based on Figure Q6(d).

(12 marks)

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FINAL EXAM

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Table 2: Laplace Transform Theorem

ltem no.	Theorem		Name
1.	$\mathscr{L}[f(t)] = F(s)$	$= \int_{0^{-}}^{\infty} f(t) e^{-st} dt$	Definition
2.	$\mathcal{L}[kf(t)]$	= kF(s)	Linearity theorem
3.	$\mathcal{L}[f_1(t)+f_2(t)]$	$= F_1(s) + F_2(s)$	Linearity theorem
4.	$\mathscr{L}[e^{-at}f(t)]$	= F(s+a)	Frequency shift theorem
5.	$\mathscr{L}[f(t-T)]$	$= e^{-sT}F(s)$	Time shift theorem
6.	$\mathcal{L}[f(at)]$	$= \frac{1}{a}F\left(\frac{s}{a}\right)$	Scaling theorem
7.	$\mathscr{L}\left[\frac{df}{dt}\right]$	= sF(s) - f(0-)	Differentiation theorem
8.	$\mathscr{L}\left[\frac{d^2f}{dt^2}\right]$	$= s^2 F(s) - sf(0-) - \dot{f}(0-)$	Differentiation theorem
9.	$\mathscr{L}\left[\frac{d^nf}{dt^n}\right]$	$= s^{n}F(s) - \sum_{k=1}^{n} s^{n-k}f^{k-1}(0-)$	Differentiation theorem
10.	$\mathscr{L}\left[\int_{0-}^{t}f(\tau)d\tau\right]$	$=\frac{F(s)}{s}$	Integration theorem
11.	$f(\infty)$	$=\lim_{s\to 0} sF(s)$	Final value theorem ¹
12.	<i>f</i> (0+)	$= \lim_{s \to \infty} sF(s)$	Initial value theorem ²

¹ For this theorem to yield correct finite results, all roots of the denominator of F(s) must have negative real parts and no more than one can be at the origin. ² For this theorem to be valid, f(t) must be continuous or have a step discontinuity at t = 0 (i.e.,

² For this theorem to be valid, f(t) must be continuous or have a step discontinuity at t = 0 (i.e., no impulses or their derivatives at t = 0).

