

UNIVERSITI TUN HUSSEIN ONN MALAYSIA

FINAL EXAMINATION **SEMESTER II SESSION 2016/2017**

TERBUKA

COURSE NAME

: ELECTRIC CIRCUITS

COURSE CODE

: BEL 10103

PROGRAMME

: BEJ

EXAMINATION DATE : JUNE 2017

DURATION

: 3 HOURS

INSTRUCTION

: ANSWER ALL QUESTIONS

THIS QUESTION PAPER CONSISTS OF SEVEN (7) PAGES

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Explain what is passive sign convention. Q1

(6 marks)

The numerical values of the voltages and currents in the interconnection seen in (b) Figure Q1(b) are given in Table Q1(b). Verify if the interconnection satisfy the power check.

(10 marks)

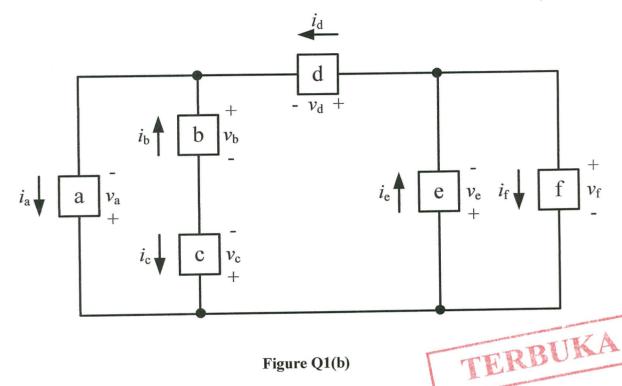


Figure Q1(b)

Table O1(b)

Table Q1(b)		
Element	Voltage (kV)	Current (µA)
a	-3	-250
b	4	-400
c	1	400
d	1	150
e	-4	200
f	4	50

An energy source forces a constant current of 1 A for 15 s flow through a light bulb. If 1.5 kJ is given off in the form of light and heat energy, calculate the voltage drop across the bulb.

(4 marks)

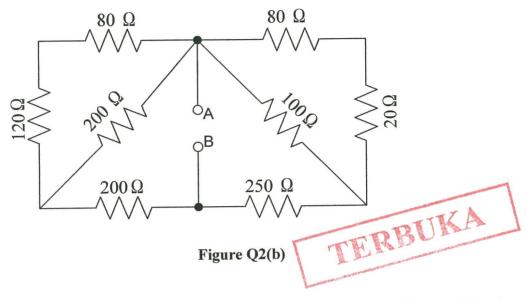
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Q2 (a) Briefly explain the current divider concept by the aid of diagram.

(2 marks)

(b) Find equivalent R_{ab} of the circuit in **Figure Q2(b)**.

(6 marks)



(c) The current flow through 20 Ω resistor in the circuit as shown in **Figure Q2(c)** is 1A, calculate V_s . (12 marks)

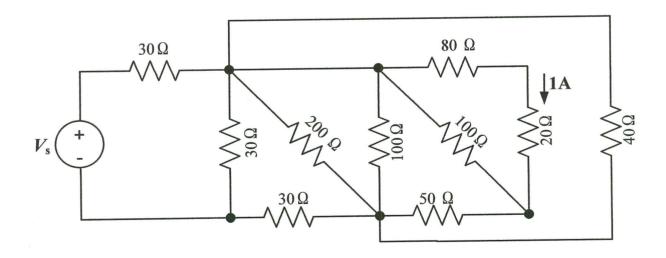


Figure Q2(c)

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Q3 (a) Explain the concept of **supermesh** in mesh analysis.

(5 marks)

(b) Find the v_0 in Figure Q3(b).

(15 marks)

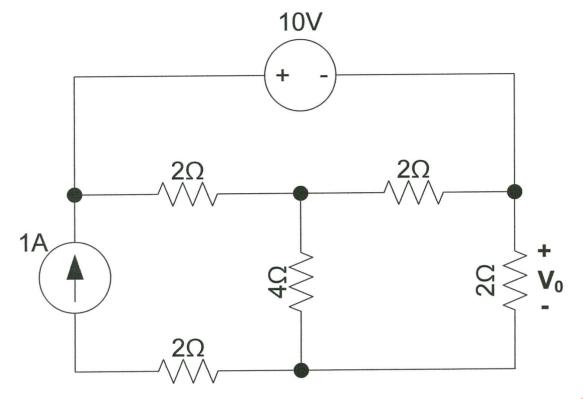


Figure Q3(b)



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Q4 (a) List down steps for synthesizing a Thevenin equivalent circuit.

(3 marks)

(b) For the circuit in Figure Q4(b),

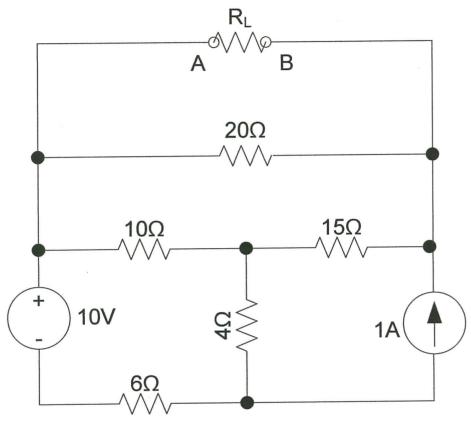


Figure Q4(b)

(i) Obtain the value of R_L for maximum power transfer, P_{max} to occur.

(5 marks)

(ii) Calculate the P_{max} .

(12 marks)



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Consider the circuit shown in Figure Q5(a), determine the equivalent capacitance Q5 (a) between terminal A-B

(4 marks)

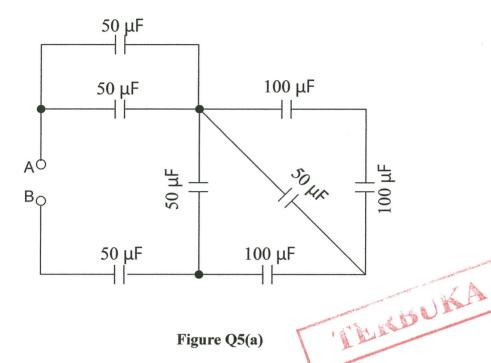
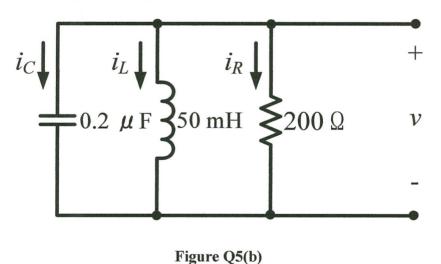


Figure Q5(a)

For the circuit in Figure Q5(b),



Obtain the root of the characteristic s_1 and s_2 . (i)

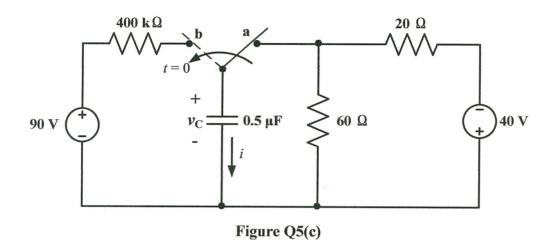
(4 marks)

State the type of response. (ii)

(2 marks)

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(c) The switch in the circuit shown in **Figure Q5(c)** has been in position **a** for a long time. At t = 0, the switch is moved to position **b**.



(i) Analyse the expression for $v_C(t)$ for $t \ge 0$.

(6 marks)

(ii) Analyse the expression for i(t) for $t \ge 0$.

(4 marks)

- END OF QUESTIONS -

