

# UNIVERSITI TUN HUSSEIN ONN MALAYSIA

## FINAL EXAMINATION (ONLINE) SEMESTER II SESSION 2020/2021

COURSE NAME	:	FUNDAMENTAL OF SEMICONDUCTOR
COURSE CODE	:	BEJ 33103
PROGRAMME CODE	4	BEJ
EXAMINATION DATE	:	JULY 2021
DURATION	:	3 HOURS
INSTRUCTION	:	ANSWER ALL QUESTIONS OPEN BOOK EXAMINATION PLEASE UPLOAD YOUR ANSWER IN PDF FORM TO AUTHOR (UTHM LMS) WITHIN THE SPECIFIED TIME GIVEN.

TERBUKA

THIS QUESTION PAPER CONSISTS OF FIVE (5) PAGES

BEJ33103

- Q1 (a) Defects in a semiconductor material introduce allowed energy states within the forbidden bandgap. Assume that a particular defect in silicon introduces two discrete levels: a donor defect level 0.25 eV above the top of the valence band, and an acceptor defect level 0.65 eV above the top of the valence band.
  - (i) Analyze which defect level dominates in the semiconductor. With the aid of energy band diagram, justify your answer.

(6 marks)

(ii) Calculate the carrier concentration and Fermi level position with respect to the intrinsic Fermi level if the semiconductor is doped with  $4.25 \times 10^{16}$  antimony/cm<sup>3</sup> and  $8.74 \times 10^{14}$  indium/cm<sup>3</sup> at 300 K.

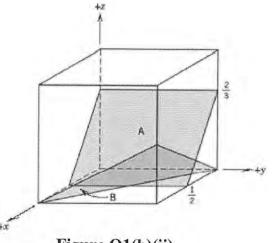
(7 marks)

- (b) The properties of semiconductors are determined to a large extent by the crystal structure.
  - (i) Identify the number of atoms of Si unit cell on (111) plane.

(2 marks)

(ii) Determine the miller indices for plane A in Figure Q1(b)(ii).

(4 marks)





(c) Explain the electrical conductivities correspond to the following material:

(1) Conductor	
(ii) Insulator	(2 marks)
(iii) Semiconductor	(2 marks)
	(2 marks)

TERBUKA

2

BEJ33103

Sketch the plot of average drift velocity as a function of applied electric field for Q2 (a) electron and hole in silicon.

(1 mark)

(b) Based on the plot in Q2(a), determine the step to obtain mobility.

(2 marks)

(c) Another technique for determining the conductivity type of a semiconductor is called the hot probe method. It consists of two probes and an ammeter that indicates the direction of the current. One probe is heated and the other is at room temperature. No voltage is applied, but a current will exist when the probes touch the semiconductor. With the aid of a diagram, explain the operation of the hot probe technique and relate it with carrier transport phenomena.

(6 marks)

With the aid of a diagram, discuss the effect of lattice scattering and impurity (d) scattering towards mobility as a function of temperature

(6 marks)

- Design an ideal transistor based on the properties in TABLE Q2(e)(i). (e) (i) (4 marks)
  - Discuss THREE (3) bottlenecks of an ideal transistor to be the next (ii) generation of transistor.

(6 marks)

	Si	Ge	GaAs	InAs
$\mu_n$ (cm <sup>2</sup> /V·s)	1400	3900	8500	30000
$\mu_p$ (cm <sup>2</sup> /V·s)	470	1900	400	500

TADLE ON AN

- Q3 (a) Sketch a fully labelled diagram of each of the following distributions for an N<sup>+</sup>-P junction:
  - (i) electrostatic potential

charge density

electric field

(ii)

(iii)

(2 marks)

(2 marks)

(2 marks)

(b) Describe ONE (1) physical mechanism that give rise to the reverse bias breakdown in P-N junction. Explain briefly with the aid of appropriate diagram.

TER

(4 marks)

Q4

BEJ33103

(c)	For a	silicon one-sided abrupt P <sup>+</sup> -N junction with $N_A = 4 \times 10^{17}$	cm <sup>-3</sup> and
	$N_D = 3$	x $10^{14}$ cm <sup>-3</sup> at 170 K and zero bias, calculate:	
	(i)	built-in potential	(3 marks)
			(5 marks)
	(ii)	depletion layer width	(3 marks)
	(iii)	maximum electric field	
			(3 marks)
(d)	Sketch conditi	the energy band diagram and circuit of a N-P junction for the on:	following
	(i)	Thermal equilibrium condition	
			(2 marks)
	(ii)	Forward bias condition	
			(2 marks)
	(iii)	Reverse bias condition	
			(2 marks)
(a)	With th substra	he aid of energy band diagram, explain the MOS diode operation v te in accumulation, depletion and inversion modes.	vith n-type
			(6 marks)

(b) Explain **TWO (2)** approaches to overcome the problems arising from the scaling of MOSFET.

(8 marks)

(c) Predict the future of Moore's Law as number of transistors in a chip no longer doubles in every 18 months since 2008. Give **FIVE** (5) reasons to justify your answer.

(11 marks)

## -END OF QUESTIONS-

4

TERBUKA

BEJ33103

#### FINAL EXAMINATION

SEMESTER/SESSION : II / 2020/2021 COURSE NAME : FUNDAMENT

: FUNDAMENTAL OF SEMICONDUCTOR

PROGRAMME CODE: BEJ COURSE CODE: BEJ 33103

#### **Physical constant**

Boltzmann constant,  $k = 8.6173324 \times 10^{-5} \text{ eV/K}$  or  $1.38066 \times 10^{-23} \text{ J/K}$ 

Thermal voltage at 300 K, kT/q = 0.025852 V

Permittivity in vacuum,  $\varepsilon_0 = 8.85418 \text{ x } 10^{-14} \text{ F/cm}$ 

Elementary charge,  $q = 1.60218 \ge 10^{-19} \text{ C}$ 

#### Properties of Si and GaAs at 300 K

Properties	Si	GaAs	
Effective density of states in conduction band, $N_{\rm C}$ (cm <sup>-3</sup> )	2.86 x 10 <sup>19</sup>	4.7 x 10 <sup>17</sup>	
Effective density of states in valence band, $N_V$ (cm <sup>-3</sup> )	2.66 x 10 <sup>19</sup>	$7.0 \ge 10^{18}$	
Dielectric constant	11.9	12.4	
$n_i$ (cm <sup>-3</sup> )	9.65 x 10 <sup>9</sup>	2.25 x 10 <sup>6</sup>	
Energy gap (eV)	1.12	1.42	
Mobility (cm <sup>2</sup> /V-s) $\mu_n$ (electrons)	1450	9200	
$\mu_{\rm p}$ (holes)	505	320	

